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Chap. 2 2 VLSI Digital Signal Processing Systems

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precision is not required, and is therefore an ideal solution for the implementation of perception systems. The possibility to choose the physical variable that represents each signal allows all the features of the transistor to be exploited

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Additional info for VLSI Signal Processing Technology. Sample text. As CPU capabilities increase, we expect some of these functions to migrate to the CPU. However, a simple calculation of the MIPS (million instructions per second) requirements for these compression schemes indicates that such an eventuality is at least five years away. For example, in Table 2, we show the MIPS requirements for

VLSI Optimization for Signal Processing Systems : vTools ...

VLSI Optimization for Signal Processing Systems As VLSI technology continues to scale down, more and more complex signal processing algorithms such as Low-Density Parity-Check (LDPC) coding, MIMO detection, and Maximum Likelihood Sequence Estimation (MLSE) are integrated into a single chip to improve the system performance, which usually lead

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